Manufacturing of nanodevices

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Methods and requirements

- UP BOTTOM technique size reduction of micrometer-sized structures down to sizes of I-100 nm
- BOTTOM UP technique nm sized structures are built from atoms
- The purity requirement for Si-based nanodevices : I:10⁹
- The purity requirement for Ge-based nanodevices : I:10¹³

Growth of monocrystals

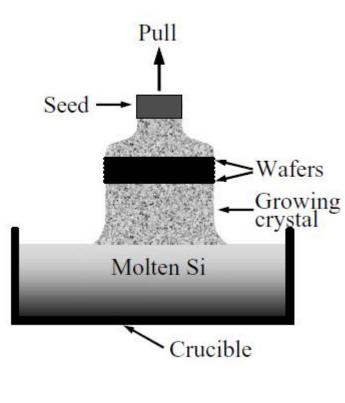
 SiO_2 + 2C \rightarrow Si + 2CO (on 1800 °C) The obtained Si is not a monocrystal and not

pure enough Si + 3HCl \rightarrow HSiCl₃ + H₂

Trichlorosilane is liquid but contanimated Trichlorosilane can be separated from impurities by fractional distillation. $HSiCl_{3} + 2 H_{2} \rightarrow 2Si + 6HCl$ The resulting Si is highly pure, but polycrystalline!

Growth of monocrystals

 The Czochralski method (zone melting - at I4I2 °C)

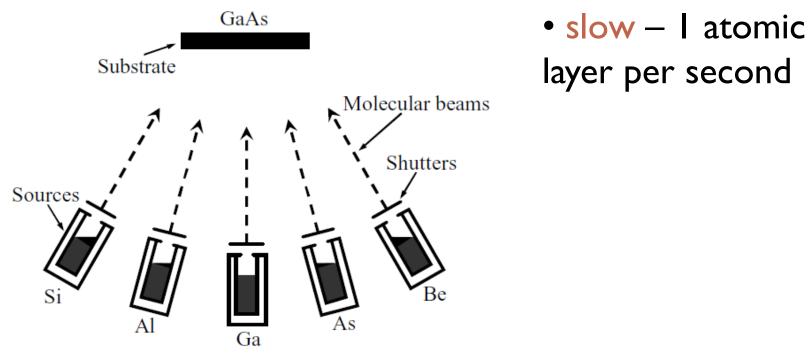


- wafers (h ~ 100 μm) are made from the resulting monocrystal cylinder
- these wafers can serve as a good basis for designing nanodevices on their surfaces (chemical, crystallographic, etc.
 fitting problems)

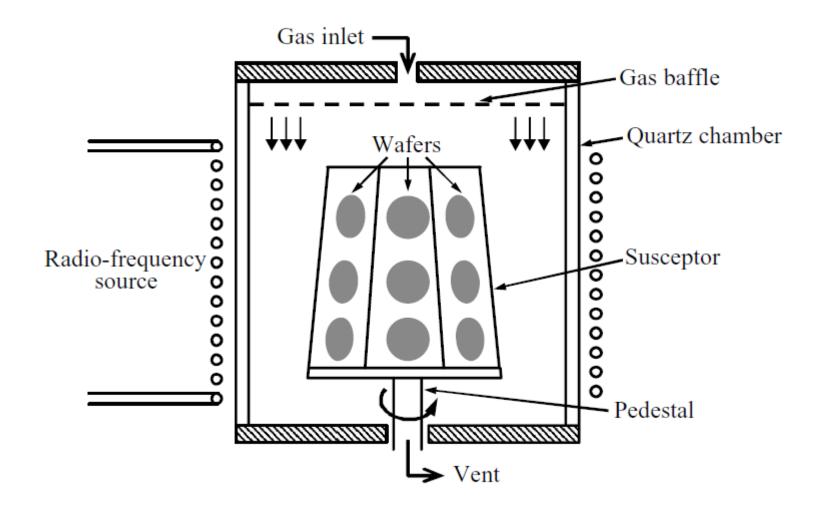


Molecular beam epitaxial growth

 Formation of single-crystal thin layers in vacuum under controlled conditions on a base plate e.g. fabrication of heterostructures



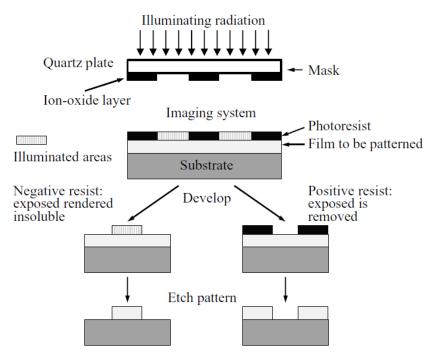
Deposition of chemical vapors





Nanolitography

 It is not possible to do lithography with visible light at nanoscale, because the wavelength of the light is too long → UV, X-ray, electron beam lithography is needed



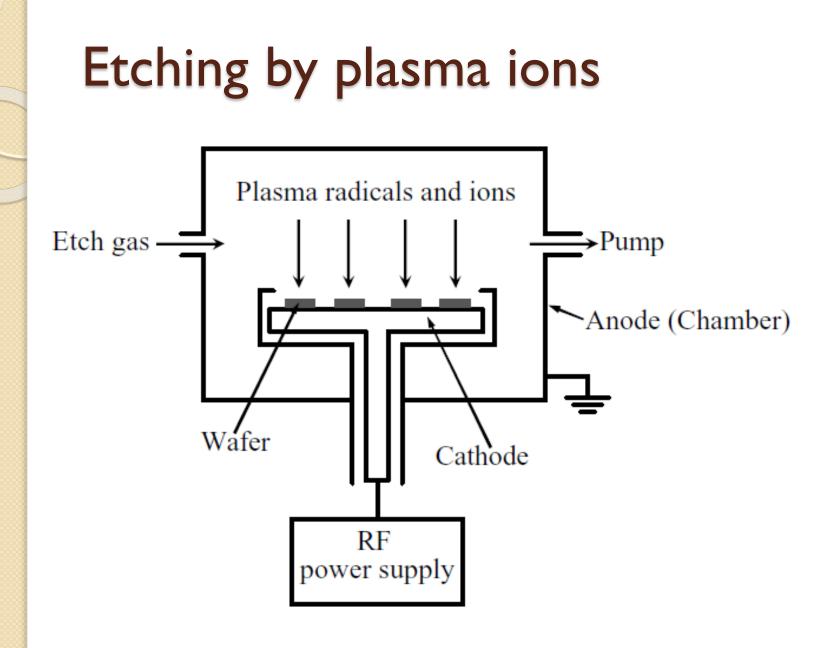
- making nanochannels and quantum wires on compatible surfaces
- making quantum dots on compatible surfaces
- etching is the last
 step



Etching processes

• Dry etching technologies (electron beam, ion beam or UV laser etching)

Wet etching technologies (chemical etching processes)

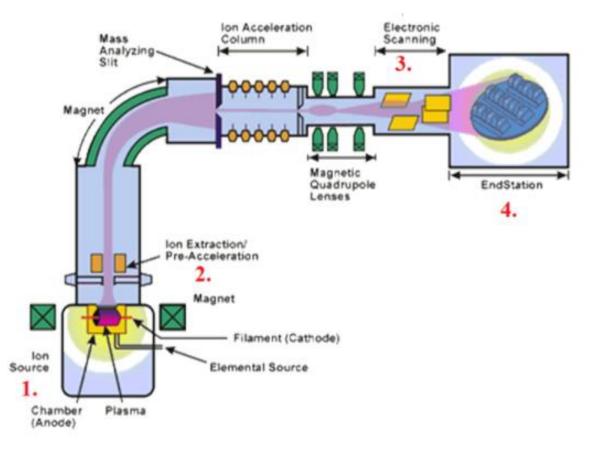




Ionimplantation

Berendezés:

- 1. ionforrás
- gyorsítás
- pásztázás
- 4. mintakamra (target)



Single atom deposition (STM)

 Placement of individual atoms on a compatible surface - the principle of a scanning tunneling microscope

